



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Laurence B. Boucher, et al. Ser. No: 10/634,062
Filing Date: August 4, 2003 Examiner: Unknown
Atty. Docket No: ALA-008G GAU: Unknown
For: METHOD AND APPARATUS FOR DATA RE-ASSEMBLY WITH A
HIGH PERFORMANCE NETWORK INTERFACE

January 26, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement per 37 C.F.R. §1.98

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring the following documents to the Examiner's attention. Included are forty-three non-patent reference documents and a three-page form PTO-1449 listing those documents, nine foreign documents and a one-page form PTO-1449 listing those documents, and a four-page form PTO-1449 listing one hundred-seven U.S. Patent reference documents. Copies of the U.S. Patent documents are not enclosed.

Citation of these documents shall not be construed as an admission that these documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).


Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 26, 2004.

Date: 1-26-04


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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 10/634,062

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Inventors: Laurence B. Boucher et al.

Group Art Unit: unknown

Examiner name: unknown

Attorney Docket No.: ALA-008G


**METHOD AND APPARATUS FOR DATA RE-ASSEMBLY
WITH A HIGH PERFORMANCE NETWORK INTERFACE**

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	2003/0165160	9/4/03	Minami et al.			
	B	2003/0066011	4/11/02	Oren			
	C	2001/0053148	12/20/01	Bilic et al.			
	D	2001/0048681	12/6/01	Bilic et al.			
	E	2001/0025315	01/10/01	Jolitz			
	F	2001/0014954	8/16/01	Purcell et al.			
	G	2001/0014892	8/16/01	Gaither et al.			
	H	2001/0013059	8/9/01	Dawson et al.			
	I	2001/0004354	01/10/01	Jolitz			
	J	4,366,538	12/28/82	Johnson et al.	364	200	
	K	4,589,063	5/13/86	Shah et al.	710	8	
	L	4,991,133	5/13/86	Davis et al.	364	900	
	M	5,056,058	10/8/91	Hirata et al.	709	230	
	N	5,056,058	10/8/91	Hirata, et al.	364	900	
	O	5,058,110	10/15/91	Beach et al.	370	85.6	
	P	5,097,442	3/17/92	Ward et al.	365	78	
	Q	5,163,131	11/10/92	Row et al.	395	200	
	R	5,212,778	5/18/93	Dally et al.	395	400	
	S	5,280,477	1/18/94	Trapp	370	85.1	
	T	5,289,580	2/22/94	Latif et al.	395	275	
	U	5,303,344	4/12/94	Yokoyama et al.	395	200	
	V	5,412,782	5/2/95	Hausman et al.	395	250	
	W	5,448,566	9/5/95	Richter et al.	370	94.1	
	X	5,485,579	1/16/96	Hitz et al.	395	200.12	
	Y	5,506,966	4/9/96	Ban	395	250	
	Z	5,511,169	4/23/96	Suda	395	280	
	AA	5,548,730	8/20/96	Young et al.	395	280	
	AB	5,558,121	12/24/96	Reddin et al.	395	200.15	

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U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AC	5,566,170	10/15/96	Bakke et al.	370	60	
	AD	5,590,328	12/31/96	Seno et al.	395	675	
	AE	5,592,622	1/7/97	Isfeld et al.	395	200.02	
	AF	5,629,933	5/13/97	Delp et al.	370	411	
	AG	5,634,099	5/27/97	Andrews et al.	395	200.07	
	AH	5,634,127	5/27/97	Cloud et al.	395	680	
	AI	5,642,482	6/24/97	Pardillos	395	200.2	
	AJ	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	AK	5,671,355	9/23/97	Collins	395	200.2	
	AL	5,678,060	10/14/97	Yokoyama et al.	709	212	
	AM	5,692,130	11/25/97	Shobu et al.	395	200.12	
	AN	5,699,317	12/16/97	Sartore et al.	395	230.06	
	AO	5,701,434	12/23/97	Nakagawa	395	484	
	AP	5,701,516	12/23/97	Cheng et al.	395	842	
	AQ	5,749,095	5/5/98	Hagersten	711	141	
	AR	5,751,715	5/12/98	Chan et al.	370	455	
	AS	5,752,078	5/12/98	Delp et al.	395	827	
	AT	5,758,084	5/26/98	Silverstein et al.	395	200.58	
	AU	5,758,089	5/26/98	Gentry et al.	395	200.64	
	AV	5,758,186	5/26/98	Hamilton et al.	395	831	
	AW	5,758,194	5/26/98	Kuzma	395	886	
	AX	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	AY	5,778,013	7/7/98	Jedwab	714	807	
	AZ	5,790,804	8/4/98	Osborne	709	245	
	BA	5,794,061	08/11/98	Hansen et al.	395	800.01	
	BB	5,802,580	09/01/98	McAlpice	711	149	
	BC	5,809,328	09/15/98	Nogales et al.	395	825	
	BD	5,812,775	9/22/98	Van Seeters et al.	395	200.43	
Examiner			Date Considered				
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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BE	5,815,646	09/29/98	Purcell et al.	395	163	
	BF	5,878,225	3/2/99	Bilansky et al.	395	200.57	
	BG	5,913,028	6/15/99	Wang, et al.	395	200.33	
	BH	5,930,830	7/27/99	Mendelson et al.	711	171	
	BI	5,931,918	08/03/99	Row et al.	709	300	
	BJ	5,935,205	08/10/99	Murayama et al.	709	216	
	BK	5,937,169	08/10/99	Connery et al.	395	200.8	
	BL	5,941,969	08/24/99	Ram et al.	710	128	
	BM	5,941,972	08/24/99	Hoese et al.	710	129	
	BN	5,950,203	09/07/99	Stakuis et al.	707	10	
	BO	5,970,804	8/4/98	Osborne	395	200.75	
	BP	5,991,299	11/23/99	Radogna et al.	370	392	
	BQ	5,996,024	11/30/99	Blumenau	709	301	
	BR	6,005,849	12/21/99	Roach et al.	370	276	
	BS	6,009,478	12/28/99	Panner, et al.	710	5	
	BT	6,016,513	01/18/00	Glen H. Lowe	709	250	
	BU	6,021,446	02/01/00	Gentry et al.	709	303	
	BV	6,026,452	02/15/00	William Michael Pitts	710	56	
	BW	6,034,963	3/7/00	Minami et al.	370	401	
	BX	6,038,562	3/14/00	Anjur et al.	707	10	
	BY	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	
	BZ	6,047,356	04/04/00	Anderson et al.	711	129	
	CA	6,057,863	05/02/00	Sompong P. Olarig	345	520	
	CB	6,061,368	5/9/00	Hitzelberger	370	537	
	CC	6,065,096	05/16/00	Day et al.	711	114	
	CD	6,070,200	5/30/00	Gates et al.	710	20	
	CE	6,101,555	8/8/00	Goshey et al.	709	321	
	CF	6,141,705	10/31/00	Anand et al.	710	15	

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U.S. Patent Documents

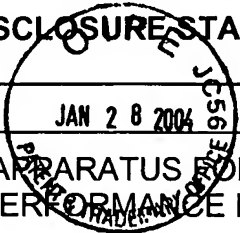
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	CG	6,145,017	11/7/00	Ghaffari	710	5	
	CH	6,157,955	12/5/00	Narad et al.	709	228	
	CI	6,172,980	1/9/01	Flanders et al.	370	401	
	CJ	6,173,333	07/18/97	Jolitz et al.			
	CK	6,202,105	3/13/01	Gates et al.	710	20	
	CL	6,226,680	05/01/01	Boucher et al.	709	230	
	CM	6,246,683	06/12/01	Connery et al.	370	392	
	CN	6,247,060	06/12/01	Boucher et al.	709	238	
	CO	6,279,051	8/21/01	Gates et al.	710	20	
	CP	6,298,403	10/2/01	Suri et al.	710	100	
	CQ	6,334,153	12/25/01	Boucher et al.	709	230	
	CR	6,345,301	02/05/02	Burns et al.	709	230	
	CS	6,345,301	02/05/02	Burns et al.	709	230	
	CT	6,356,951	03/12/02	Gentry et al.	709	250	
	CU	6,389,468	05/14/02	Muller et al.	709	226	
	CV	6,389,479	5/14/02	Boucher	709	243	
	CW	6,421,742	7/16/02	Tillier	710	1	
	CX	6,427,169	07/30/02	Elzur	709	224	
	CY	6,434,651	08/13/02	Gentry, Jr.	710	260	
	CZ	6,449,656	09/10/02	Elzur et al.	709	236	
	DA	6,453,360	09/17/02	Muller et al.	709	250	
	DB	6,480,489	11/12/02	Muller et al.	370	389	
	DC	6,526,446	2/25/03	Yang et al.	709	230	
	DD						
	DE						
	DF						
	DG						

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Foreign Patent Documents								
*Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation	
	A	WO 98/50852	11/12/98	PCT/US98/0871			Yes	No
	B	WO 99/04343	1/28/99	PCT/US98/14729				
	C	WO 99/65219	12/16/99	PCT/US/99/13184				
	D	WO 00/13091	3/09/00	PCT/US98/24943				
	E	WO 01/04770	1/18/01	PCT/US00/18939				
	F	WO 01/05107	1/18/01	PCT/US00/19006				
	G	WO 01/05116	1/18/01	PCT/US00/19243				
	H	WO 01/05123	1/18/01	PCT/US00/18976				
	I	WO 01/40960	6/07/01	PCT/US00/32660				
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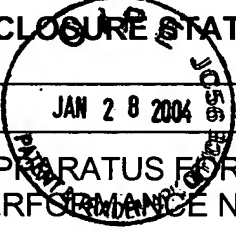
U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/634,062
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: August 4, 2003
 <p style="margin-top: 20px;">METHOD AND APPARATUS FOR DATA RE-ASSEMBLY WITH A HIGH PERFORMANCE NETWORK INTERFACE</p>		Inventors: Laurence B. Boucher et al.
		Group Art Unit: unknown
		Examiner name: unknown
		Attorney Docket No.: ALA-008G
OTHER ART—NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	1	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
	2	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
	3	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.
	4	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.
	5	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
	6	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	7	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	8	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996, 15 pages.
	9	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
	10	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
	11	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", http://www.ireadyco.com/archives/keyexec.html , 2 pages, printed 11/28/98.
	12	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998, 3 pages, printed 11/28/98.
	13	Internet pages from iReady Products, web site http://www.ireadyco.com/products.html , 2 pages, downloaded 11/25/98.
	14	iReady News Archives, Toshiba, iReady shipping Internet chip, 1 page, printed 11/25/98.
	15	Interprophet article entitled "Technology", http://www.interprophet.com/technology.html , 17 pages, printed 3/1/00.
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OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	16	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.
	17	iReady article entitled "About Us Introduction", Internet pages from http://www.iReadyco.com/about.html , 3 pages, printed 11/25/98.
	18	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, November 20, 1997. 2 pages, printed 11/2/98.
	19	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.
	20	NEWSwatch article entitled "iReady internet Tuner to Web Enable Devices", Tuesday, November 5, 1996, printed 11/2/98, 2 pages.
	21	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
	22	"Comparison of Novell Netware and TCP/IP Protocol Architectures", by J.S. Carbone, 19 pages, printed 4/10/98.
	23	Adaptec article entitled "AEA-7110C-a DuraSAN product", 11 pages, printed 10/1/01.
	24	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.
	25	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channel HBA", 6 pages, printed 10/01/01.
	26	iSCSI.com article entitled "iSCSI Storage", 2 pages, printed 10/01/01.
	27	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.
	28	iReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98, 3 pages.
	29	Internet pages of InterProphet entitled "Frequently Asked Questions", by Lynne Jolitz, printed 6/14/00, 4 pages.
	30	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
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OTHER ART—NON PATENT LITERATURE DOCUMENTS		
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	31	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
	32	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.
	33	Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.
	34	CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.
	35	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
	36	CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.
	37	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
	38	Internet pages entitled "Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, printed 6/5/97, 15 pages.
	39	Jato Technologies article entitled "Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98, 13 pages.
	40	EETimes article entitled "Enterprise System Uses Flexible Spec, dated August 10, 1998, printed 11/25/98, 3 pages.
	41	Internet pages entitled "Smart Ethernet Network Interface Cards", which Berend Ozceri is developing, printed 11/25/98, 2 pages.
	42	Internet pages of Xaqti corporation entitled "GigaPower Protocol Processor Product Review," printed 11/25/99, 4 pages.
	43	Internet pages entitled "DART: Fast Application Level Networking via Data-Copy Avoidance," by Robert J. Walsh, printed 6/3/99, 25 pages.
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